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Substitute for Form 1449A/PTO (Modified)

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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Sheet of 2

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Application Number	09/701,065	
Filing Date	February 12, 2001	
First Named Inventor:	Fabrice Geiger	
Group Art Unit	1756	
Examiner Name	Duda, Kathleen	
Attorney Docket Number	2013 USA P/DSM/PMD/JW	

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Exam. Initial*	Cit e Z oi	Offi			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM- DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation? Yes/No
		DE	_1 95-28 746	C1	Grassi, Thomas et al.	10-31-1996		No
juo		EP	0 843 348	A2	Li-Qun, Xia et al.	05-20-1998		Yes

	Examiner Signature	K. Duda	Date Considered 5-19-03
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	EME (Vision as r	Cite No¹ Include name of item (book, no¹ Item (book, no² Item) (book, no² Item (book, no² Item) (book,	Cite No¹ Include name of the author (in CAPIT. item (book, magazine, journal, set number(s), public ELBEL, NORBERT, ET AL., A New 1998 Symposium on VLSI Technology Honolulu, USA, June 9-11, 1998, pusa, IEEE, ISBN: 0-7803-4770-6/9 FISCHER, E., ET AL., Global Plana VMIC Conference, June 27-29, 1999 LEE, HAN SIN, ET AL., An Optimiz Shallow Trench Isolation (STI), 1999 Papers, Honolulu, USA, June 11-13 Electrical And Electronics Engineer Patent Abstracts Of Japan, Vol. 011 198339 A (Maeda Kazuo et al.) Aug	Filing Date First Named Inventor: Group Art Unit Examiner Name OTHER ART – NO PATENT LITERATURE DOCUMEN Cite No¹ Include name of the author (in CAPITAL LETTERS), title of the article item (book, magazine, journal, serial, symposium, catalog, etc.), de number(s), publisher, city and/or country where proposed by symposium on VLSI Technology Digest of Technical Paper Honolulu, USA, June 9-11, 1998, pages 208-209, XP000802805, USA, IEEE, ISBN: 0-7803-4770-6/98 © 1998 IEEE. FISCHER, E., ET AL., Global Planarization By Selective Depositi VMIC Conference, June 27-29, 1995, pages 247-253, XP002109 LEE, HAN SIN, ET AL., An Optimized Densification of the Filled C Shallow Trench Isolation (STI), 1996 Symposium On VLSI Techno Papers, Honolulu, USA, June 11-13, 1996, pages 158-159, XP00 Electrical And Electronics Engineers, ISBN: 0-7803-3342-X/96 © Patent Abstracts Of Japan, Vol. 015, No. 464 (E-1137), November 198339 A (Maeda Kazuo et al.) August 29, 1991, pages 211-215	Filing Date Filing Date Filing Date Filing Date First Named Inventor: Fabrice Geiger Group Art Unit 1756 Examiner Name Duda, Kathleen OTHER ART – NO PATENT LITERATURE DOCUMENTS Cite No¹ Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published ELBEL, NORBERT, ET AL., A New STI Process Based on Selective Oxide Deposition; 1998 Symposium on VLSI Technology Digest of Technical Papers, (CAT NO. 98CH36216), Honolulu, USA, June 9-11, 1998, pages 208-209, XP000802805, 1998, New York, NY,

Examiner Signature	K. Duda	Date Considered 5-19-03

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